

U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.		Serial No.		
				MIC-M082		<del>Not yet known</del>		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				Applicant(s) <u>10/665,645</u>				
				Thomas S. Wong, Robert W. Bechdolt, Phi Thai				
				Filing Date		Group		
				Herewith		Not yet known		
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA							
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
<i>Wan</i>	AQ	H. H. Berger et al., "Advanced Merged Transistor Logic By Using Schottky Junctions", Microelectronics, Vol. 7, pages 35-42, March 1976, pp. 88-95.						
	AR							
	AS							
Examiner <i>[Signature]</i>		Date Considered <u>12/27/04</u>						
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.								